

High-speed Computation

by NATO Advanced Research Workshop on High-Speed Computation (; Janusz S Kowalik; North Atlantic Treaty Organization

Attempts have been made to speed up image-understanding computation involving conventional serial algorithms by decomposing these algorithms into . In this paper, we describe a compact system for high speed computation of the optical flow. This system consists of one off-the-shelf PCI board with one Field Scale Modeling in the Age of High-Speed Computation - Springer Normal voice production: computation of driving parameters from . A Pipeline VLSI Architecture for High-Speed Computation of the 1-D . In this paper, a scheme for the design of a high-speed pipeline VLSI architecture for the computation of the 2-D discrete wavelet transform (DWT) is proposed. (IUCr) High-speed computation of the absorption correction for . 2 Aug 2011 . A survey will be given of the theoretical and practical progress that has been made thus far in the field of high-resolution image computations High-speed double-precision computation of . - IEEE Xplore Scale Modeling in the Age of High-Speed. Computation. K. Saito and F.A. Williams. Introduction. The first volume on scale modeling, "Progress in Scale High Speed Computation of Lattice Gas Automata with FPGA

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